

# DATA SHEET

## **TDA8542AT** **2 × 1.5 W BTL audio amplifier**

Product specification  
Supersedes data of 1997 Nov 14  
File under Integrated Circuits, IC01

1998 Mar 25

**2 × 1.5 W BTL audio amplifier****TDA8542AT****FEATURES**

- Flexibility in use
- Few external components
- Low saturation voltage of output stage
- Gain can be fixed with external resistors
- Standby mode controlled by CMOS compatible levels
- Low standby current
- No switch-on/switch-off plops
- High supply voltage ripple rejection
- Protected against electrostatic discharge
- Outputs short-circuit safe to ground,  $V_{CC}$  and across the load
- Thermally protected.

**GENERAL DESCRIPTION**

The TDA8542AT is a two channel audio power amplifier for an output power of  $2 \times 1.5$  W with an  $8 \Omega$  load at a 6 V supply. The circuit contains two Bridge-Tied Load (BTL) amplifiers with a complementary PNP-NPN output stage and standby/mute logic. The TDA8542AT comes in a 20-pin SO package.

**APPLICATIONS**

- Portable consumer products
- Personal computers
- Motor-driver (servo).

**QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{CC}$	supply voltage		2.2	6	18	V
$I_q$	quiescent current	$V_{CC} = 6$ V	–	15	22	mA
$I_{stb}$	standby current		–	–	10	$\mu$ A
$P_o$	output power	THD = 10%; $R_L = 8 \Omega$ ; $V_{CC} = 6$ V	1	1.5	–	W
THD	total harmonic distortion	$P_o = 0.5$ W	–	0.15	–	%
SVRR	supply voltage ripple rejection		50	–	–	dB

**ORDERING INFORMATION**

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
TDA8542AT	SO20	plastic small outline package; 20 leads; body width 7.5 mm	SOT163-1

2 × 1.5 W BTL audio amplifier

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BLOCK DIAGRAM

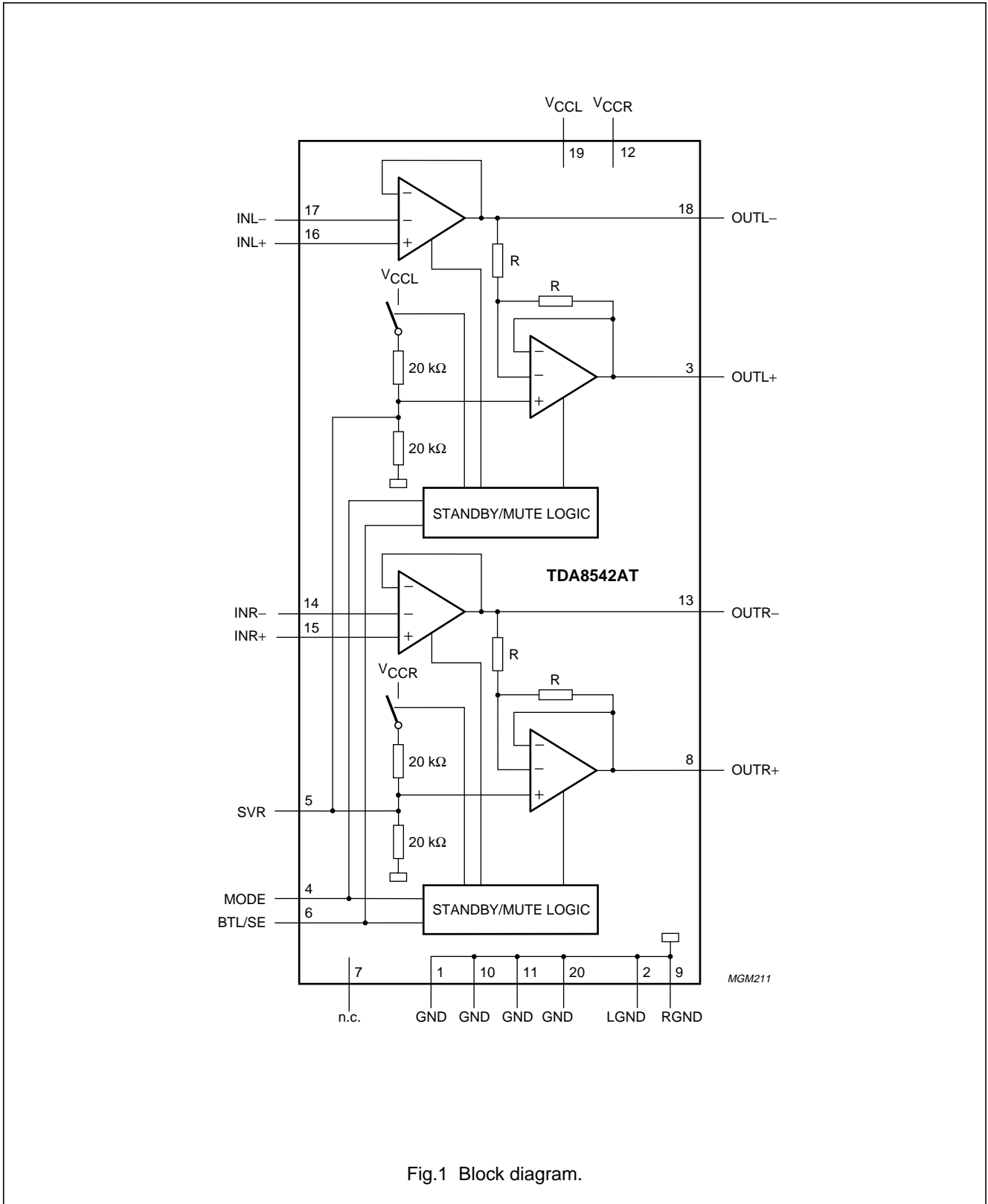


Fig.1 Block diagram.

2 × 1.5 W BTL audio amplifier

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**PINNING**

SYMBOL	PIN	DESCRIPTION
GND	1	ground; note 1
LGND	2	ground, left channel
OUTL+	3	positive loudspeaker terminal, left channel
MODE	4	operating mode select (standby, mute, operating)
SVR	5	half supply voltage, decoupling ripple rejection
BTL/SE	6	BTL loudspeaker or SE headphone operation
n.c.	7	not connected
OUTR+	8	positive loudspeaker terminal, right channel
RGND	9	ground, right channel
GND	10	ground; note 1
GND	11	ground; note 1
V <sub>CCR</sub>	12	supply voltage, right channel
OUTR-	13	negative loudspeaker terminal, right channel
INR-	14	negative input, right channel
INR+	15	positive input, right channel
INL+	16	positive input, left channel
INL-	17	negative input, left channel
OUTL-	18	negative loudspeaker terminal, left channel
V <sub>CCL</sub>	19	supply voltage, left channel
GND	20	ground; note 1

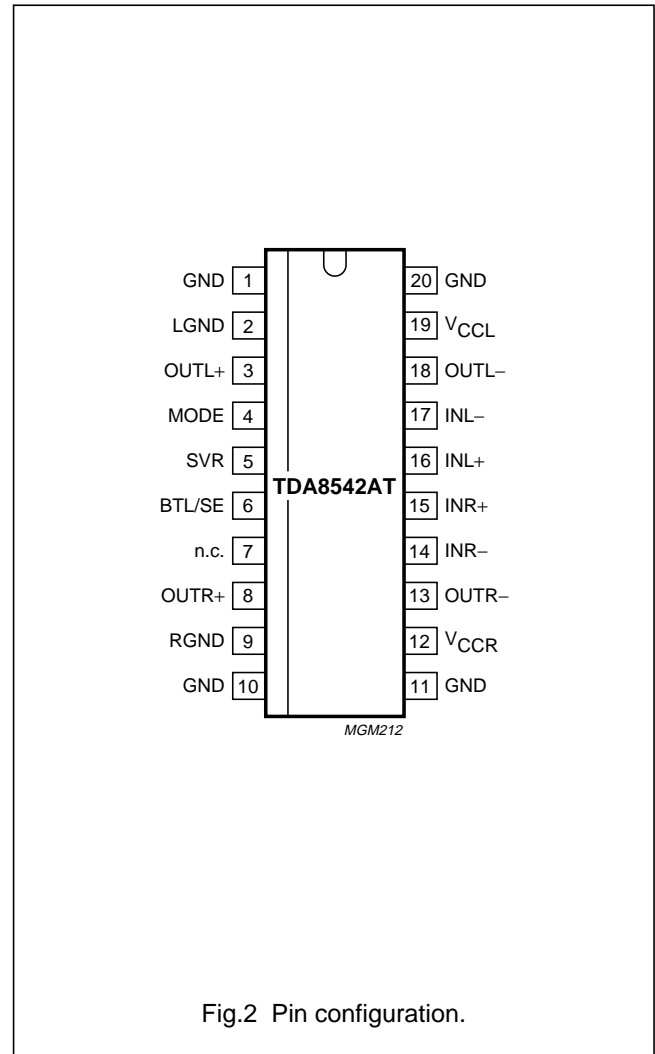


Fig.2 Pin configuration.

**Note**

1. Pins 1, 10, 11 and 20 are connected to the leadframe and also to the substrate. They may be kept floating. When connected to the ground-plane the PCB can be used as heatsink.

**2 × 1.5 W BTL audio amplifier****TDA8542AT****FUNCTIONAL DESCRIPTION**

The TDA8542AT is a 2 × 1.5 W BTL audio power amplifier capable of delivering 2 × 1.5 W output power to an 8 Ω load at THD = 10% using a 6 V power supply. Using the MODE pin the device can be switched to standby and mute condition. The device is protected by an internal thermal shutdown protection mechanism. The gain can be set within a range from 6 to 30 dB by external feedback resistors.

**Power amplifier**

The power amplifier is a Bridge-Tied Load (BTL) amplifier with a complementary PNP-NPN output stage. The voltage loss on the positive supply line is the saturation voltage of a PNP power transistor, on the negative side the saturation voltage of a NPN power transistor. The total voltage loss is <1 V and with a 6 V supply voltage and an 8 Ω loudspeaker an output power of 1.5 W can be delivered.

**Mode select pin**

The device is in the standby mode (with a very low current consumption) if the voltage at the MODE pin is  $>(V_{CC} - 0.5 \text{ V})$ , or if this pin is floating. At a MODE voltage level of less than 0.5 V the amplifier is fully operational. In the range between 1.5 V and  $V_{CC} - 1.5 \text{ V}$  the amplifier is in mute condition. The mute condition is useful to suppress plop noise at the output caused by charging of the input capacitor.

**Headphone connection**

A headphone can be connected to the amplifier using two coupling capacitors for each channel. The common GND pin of the headphone is connected to the ground of the amplifier (see Fig.14). In this case the BTL/SE pin must be either on a logic HIGH level or not connected at all.

The two coupling capacitors can be omitted if it is allowed to connect the common GND pin of the headphone jack not to ground, but to a voltage level of  $\frac{1}{2}V_{CC}$  (see Fig.14). In this case the BTL/SE pin must be either on a logic LOW level or connected to ground. If the BTL/SE pin is on a LOW level, the power amplifier for the positive loudspeaker terminal is always in mute condition.

**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CC}$	supply voltage	operating	-0.3	+18	V
$V_I$	input voltage		-0.3	$V_{CC} + 0.3$	V
$I_{ORM}$	repetitive peak output current		-	1	A
$T_{stg}$	storage temperature	non-operating	-55	+150	°C
$T_{amb}$	operating ambient temperature		-40	+85	°C
$V_{sc}$	AC and DC short-circuit safe voltage		-	10	V
$P_{tot}$	total power dissipation		-	2.2	W

**QUALITY SPECIFICATION**

In accordance with "SNW-FQ-611-E".

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	60	K/W

**2 × 1.5 W BTL audio amplifier****TDA8542AT****DC CHARACTERISTICS**

$V_{CC} = 6\text{ V}$ ;  $T_{amb} = 25\text{ °C}$ ;  $R_L = 8\ \Omega$ ;  $V_{MODE} = 0\text{ V}$ ; measured in test circuit Fig.3; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{CC}$	supply voltage	operating	2.2	6	18	V
$I_q$	quiescent current	$R_L = \infty$ ; note 1	–	15	22	mA
$I_{stb}$	standby current	$V_{MODE} = V_{CC}$	–	–	10	$\mu\text{A}$
$V_O$	DC output voltage	note 2	–	2.2	–	V
$ V_{OUT+} - V_{OUT-} $	differential output voltage offset		–	–	50	mV
$I_{IN+}, I_{IN-}$	input bias current		–	–	500	nA
$V_{MODE}$	input voltage mode select	operating	0	–	0.5	V
		mute	1.5	–	$V_{CC} - 1.5$	V
		standby	$V_{CC} - 0.5$	–	$V_{CC}$	V
$I_{MODE}$	input current mode select	$0 < V_{MODE} < V_{CC}$	–	–	20	$\mu\text{A}$
$V_{BTL/SE}$	input voltage BTL/SE pin	single-ended	0	–	0.6	V
		BTL	2	–	$V_{CC}$	V
$I_{BTL/SE}$	input current BTL/SE pin	$V_{BTL/SE} = 0$	–	–	100	$\mu\text{A}$

**Notes**

1. With a load connected at the outputs the quiescent current will increase, the maximum of this increase being equal to the DC output offset voltage divided by  $R_L$ .
2. The DC output voltage with respect to ground is approximately  $\frac{1}{2}V_{CC}$ .

$2 \times 1.5 \text{ W}$  BTL audio amplifier

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**AC CHARACTERISTICS**

$V_{CC} = 6 \text{ V}$ ;  $T_{amb} = 25 \text{ }^\circ\text{C}$ ;  $R_L = 8 \text{ } \Omega$ ;  $f = 1 \text{ kHz}$ ;  $V_{MODE} = 0 \text{ V}$ ; measured in test circuit Fig.3; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$P_o$	output power	THD = 10%	1.2	1.5	–	W
		THD = 0.5%	0.9	1.1	–	W
THD	total harmonic distortion	$P_o = 0.4 \text{ W}$	–	0.15	0.3	%
$G_{V(cl)}$	closed-loop voltage gain	note 1	6	–	30	dB
$Z_{i(dif)}$	differential input impedance		–	100	–	k $\Omega$
$V_{n(o)}$	noise output voltage	note 2	–	–	100	$\mu\text{V}$
SVRR	supply voltage ripple rejection	note 3	50	–	–	dB
		note 4	40	–	–	dB
$V_{o(mute)}$	output voltage in mute condition	note 5	–	–	200	$\mu\text{V}$
$\alpha_{cs}$	channel separation		40	–	–	dB

**Notes**

- Gain of the amplifier is  $2 \times \frac{R_2}{R_1}$  in test circuit of Fig.3.
- The noise output voltage is measured at the output in a frequency range from 20 Hz to 20 kHz (unweighted), with a source impedance of  $R_S = 0 \text{ } \Omega$  at the input.
- Supply voltage ripple rejection is measured at the output, with a source impedance of  $R_S = 0 \text{ } \Omega$  at the input. The ripple voltage is a sine wave with a frequency of 1 kHz and an amplitude of 100 mV (RMS), which is applied to the positive supply rail.
- Supply voltage ripple rejection is measured at the output, with a source impedance of  $R_S = 0 \text{ } \Omega$  at the input. The ripple voltage is a sine wave with a frequency between 100 Hz and 20 kHz and an amplitude of 100 mV (RMS), which is applied to the positive supply rail.
- Output voltage in mute position is measured with a 1 V (RMS) input voltage in a bandwidth of 20 kHz, so including noise.

**2 × 1.5 W BTL audio amplifier****TDA8542AT****TEST AND APPLICATION INFORMATION****Test conditions**

Because the application can be either Bridge-Tied Load (BTL) or Single-Ended (SE), the curves of each application are shown separately.

The thermal resistance = 60 K/W; the maximum sine wave power dissipation for  $T_{amb} = 25\text{ °C}$  is:  $\frac{150 - 25}{60} = 2.1\text{ W}$

For  $T_{amb} = 60\text{ °C}$  the maximum total power dissipation is:  $\frac{150 - 60}{60} = 1.5\text{ W}$

**BTL application**

$T_{amb} = 25\text{ °C}$  if not specially mentioned,  $V_{CC} = 6\text{ V}$ ,  $f = 1\text{ kHz}$ ,  $R_L = 8\ \Omega$ ,  $G_v = 20\text{ dB}$ , audio band-pass 22 Hz to 22 kHz.

The BTL application diagram is illustrated in Fig.3.

The quiescent current has been measured without any load impedance. The total harmonic distortion as a function of frequency was measured with a low-pass filter of 80 kHz. The value of capacitor C3 influences the behaviour of the SVRR at low frequencies, increasing the value of C3 increases the performance of the SVRR. The figure of the mode select voltage ( $V_{ms}$ ) as a function of the supply voltage shows three areas; operating, mute and standby. It shows, that the DC-switching levels of the mute and standby respectively depends on the supply voltage level.

**Thermal behaviour**

The measured thermal resistance of the IC package is highly dependent on the configuration and size of the application board. Data may not be comparable between different semiconductors manufacturers because the application boards and test methods are not (yet) standardized. Also, the thermal performance of packages for a specific application may be different than presented here, because the configuration of the application boards (copper area) may be different.

Philips Semiconductors uses FR-4 type application boards with 1 oz copper traces with solder coating. The measurements have been carried out with vertical placed boards.

Using a practical PCB layout with wider copper tracks and some copper area to the IC pins and just under the IC (see Fig.22), the thermal resistance from junction to ambient can be reduced to approximately 56 K/W.

For  $T_{amb(max)} = 50\text{ °C}$  the maximum total power dissipation at this PCB layout is:  $\frac{150 - 50}{56} = 1.79\text{ W}$

For the application  $V_{CC} = 6\text{ V}$  and  $R_L = 8\ \Omega$  the worst case sine wave dissipation is 1.75 W.

**SE application**

$T_{amb} = 25\text{ °C}$  if not specially mentioned,  $V_{CC} = 7.5\text{ V}$ ,  $f = 1\text{ kHz}$ ,  $R_L = 4\ \Omega$ ,  $G_v = 20\text{ dB}$ , audio band-pass 22 Hz to 22 kHz.

The SE application diagram is illustrated in Fig.14.

If the BTL/SE pin (pin 6) is connected to ground, the positive outputs (pins 3 and 8) will be in mute condition with a DC level of  $\frac{1}{2}V_{CC}$ . When a headphone is used ( $R_L \geq 25\ \Omega$ ) the SE headphone application can be used without output coupling capacitors; load between negative output and one of the positive outputs (e.g. pin 3) as common pin.

Increasing the value of electrolytic capacitor C3 will result in a better channel separation. Because the positive output is not designed for high output current ( $2 \times I_o$ ) at low load impedance ( $\leq 16\ \Omega$ ), the SE application with output capacitors connected to ground is advised. The capacitor value of C4/C5 in combination with the load impedance determines the low frequency behaviour. The THD as a function of frequency was measured using a low-pass filter of 80 kHz. The value of capacitor C3 influences the behaviour of the SVRR at low frequencies, increasing the value of C3 increases the performance of the SVRR.

**General remark**

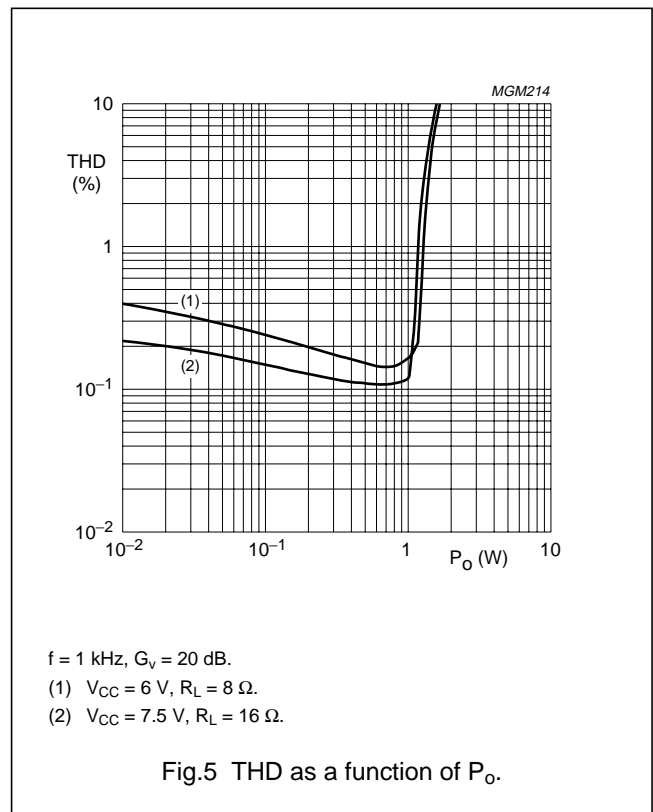
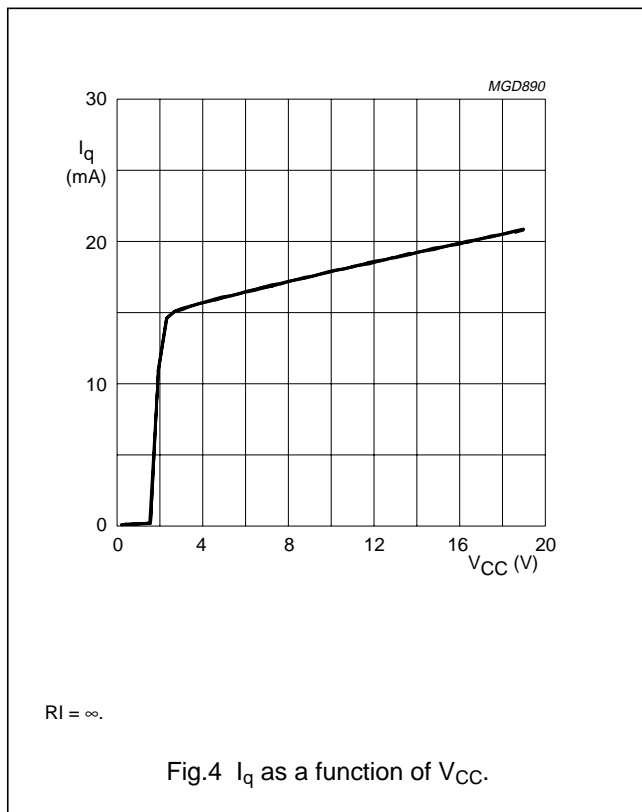
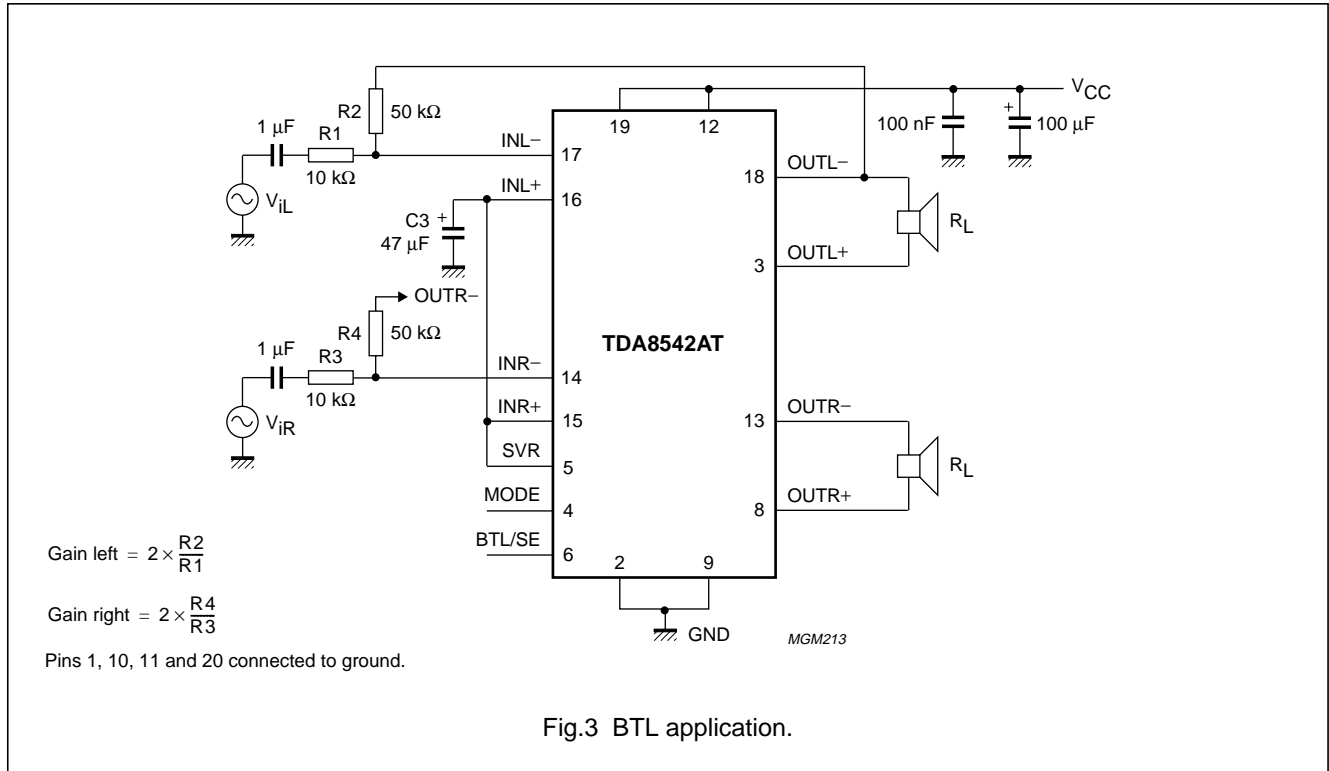
The frequency characteristic can be adapted by connecting a small capacitor across the feedback resistor. To improve the immunity of HF radiation in radio circuit applications, a small capacitor can be connected in parallel with the feedback resistor (56 k $\Omega$ ); this creates a low-pass filter.



2 × 1.5 W BTL audio amplifier

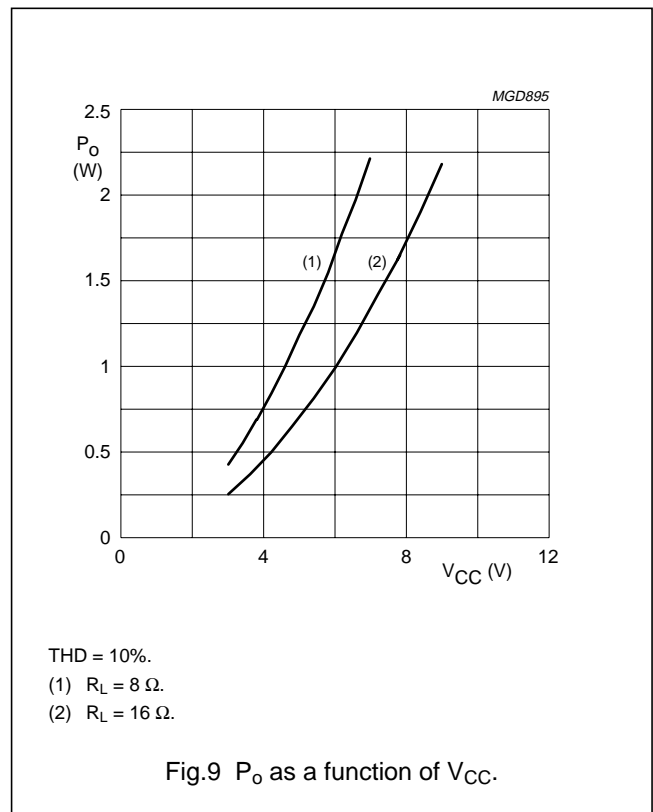
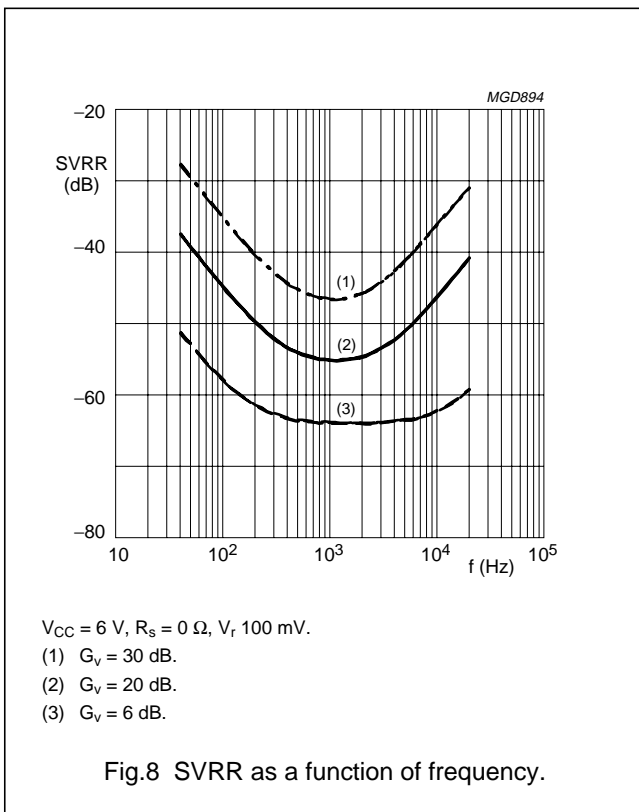
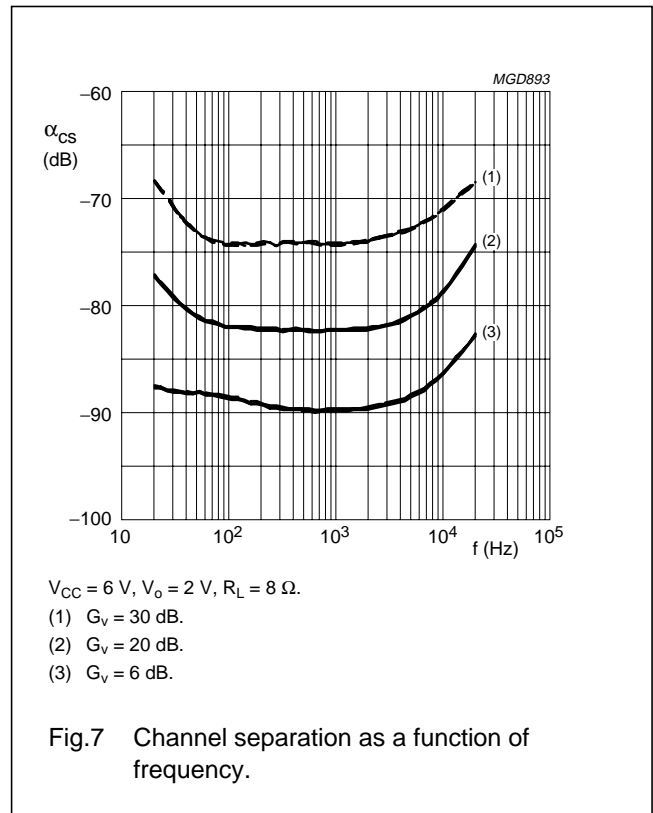
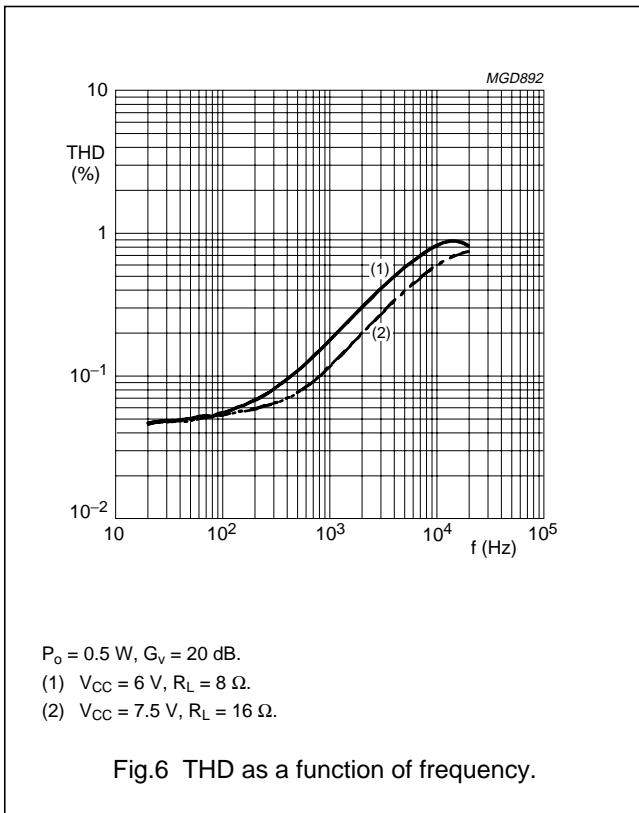
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BTL APPLICATION



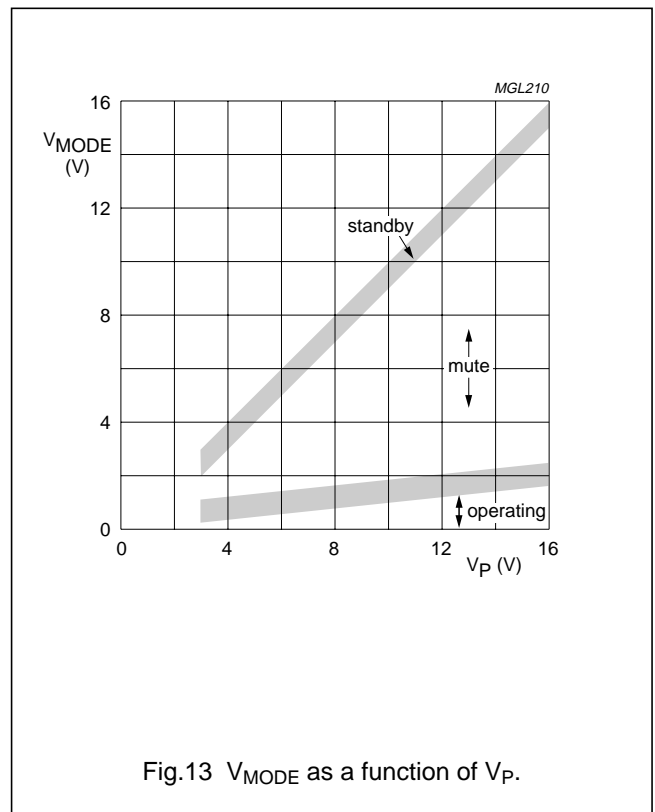
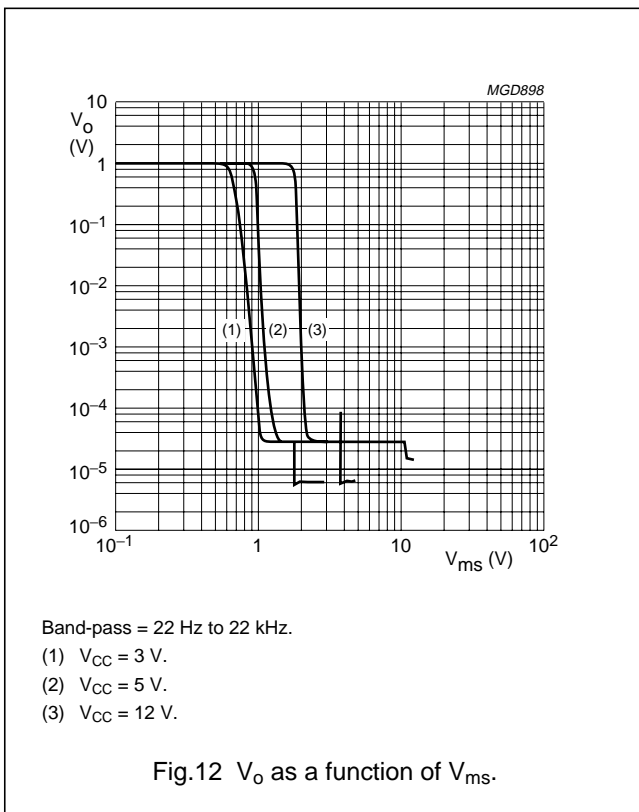
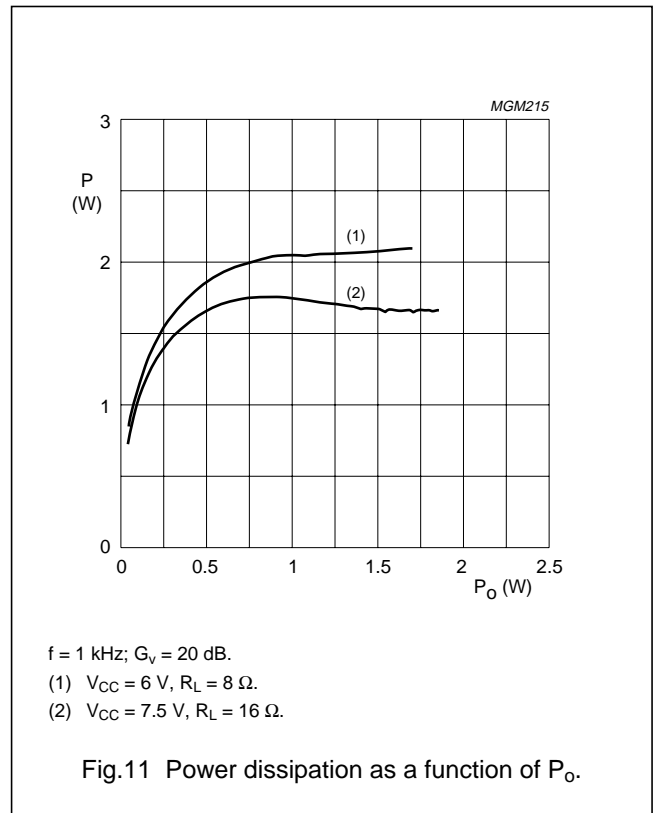
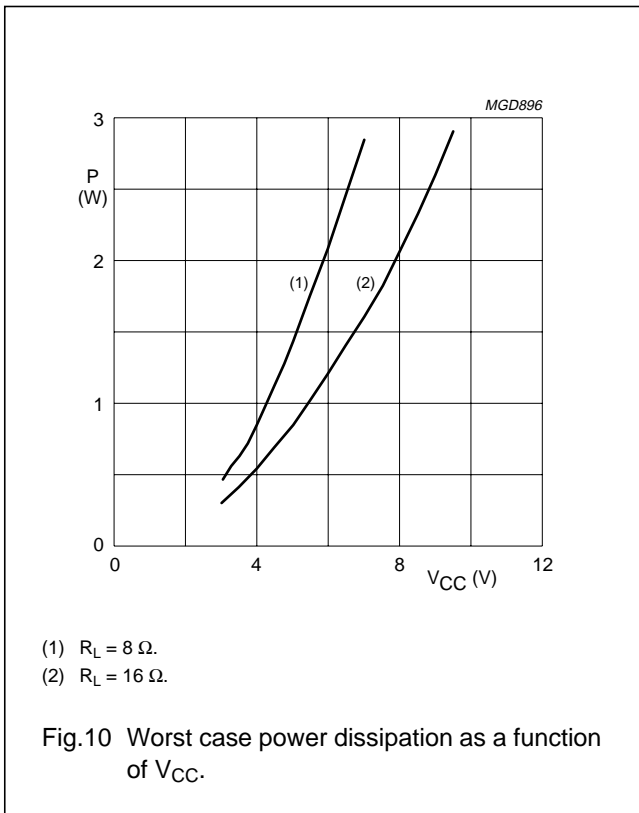
2 × 1.5 W BTL audio amplifier

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2 × 1.5 W BTL audio amplifier

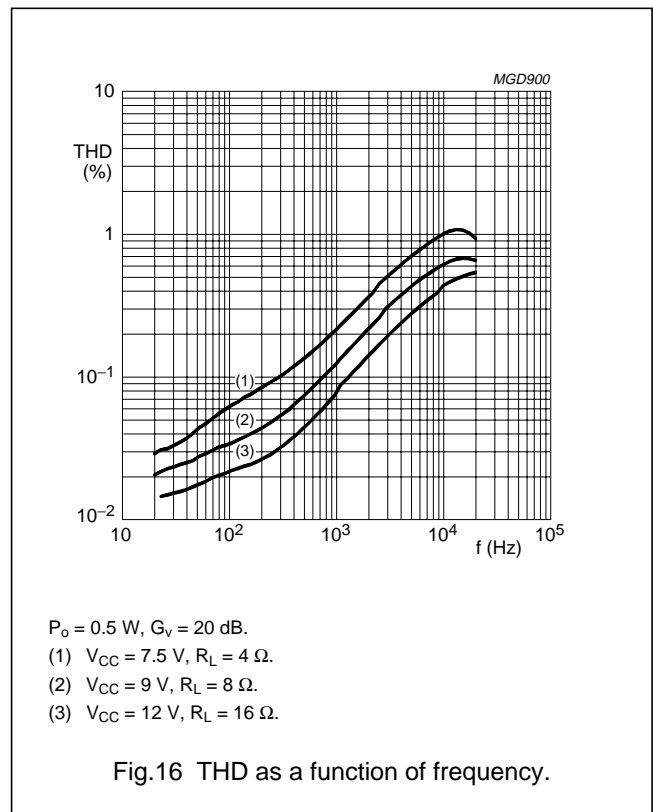
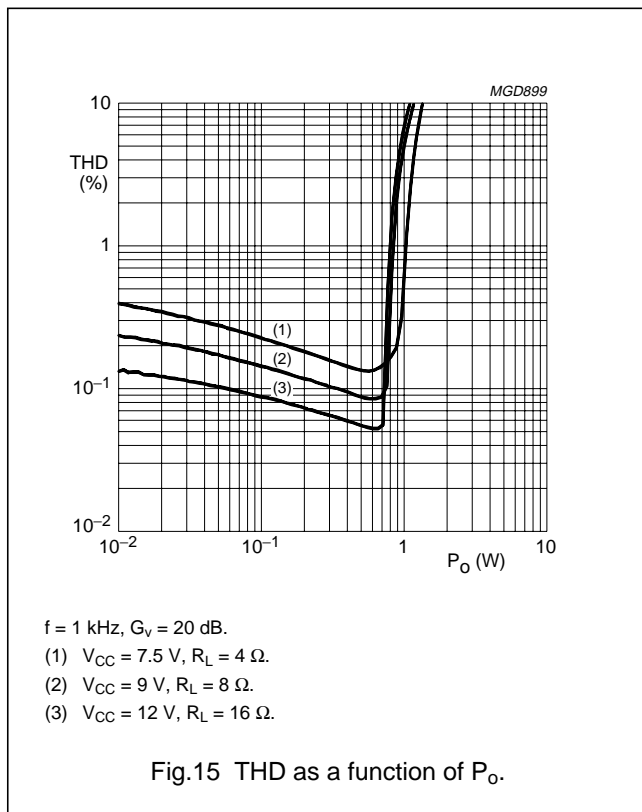
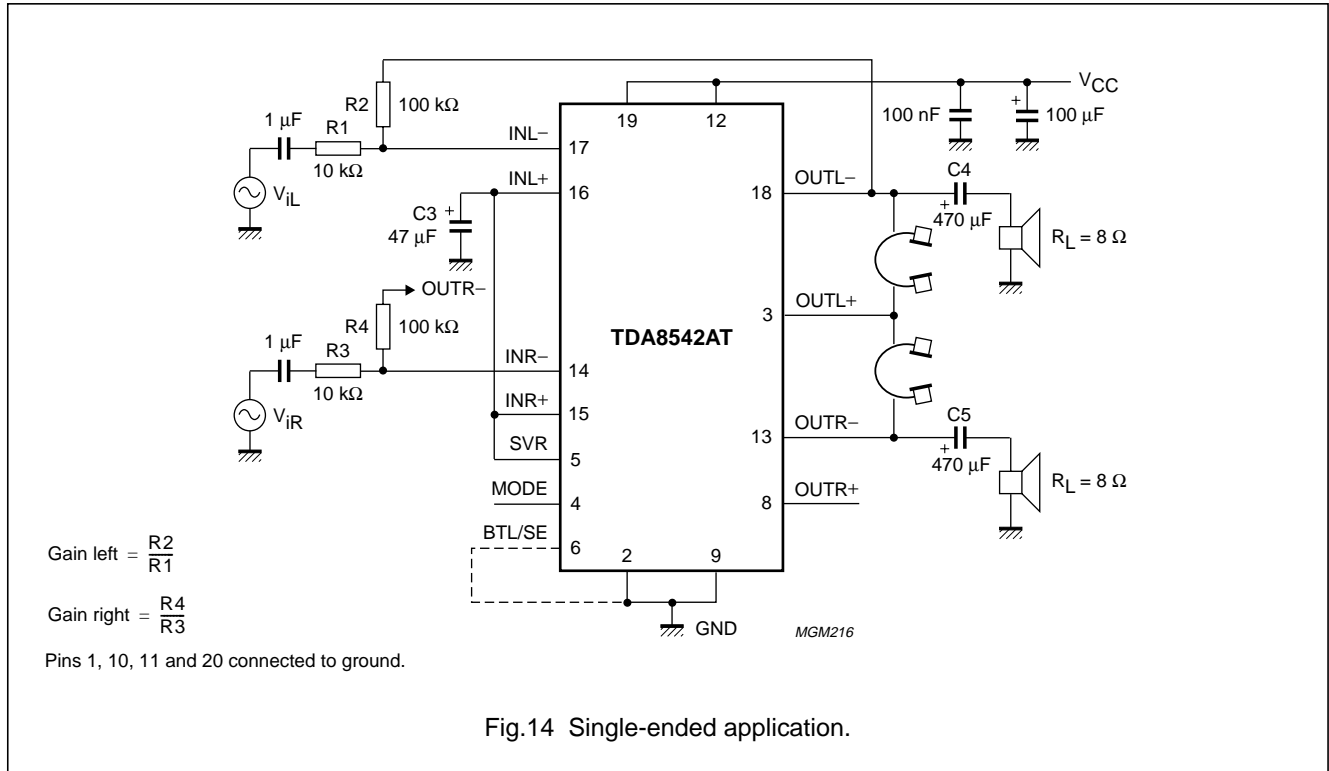
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2 × 1.5 W BTL audio amplifier

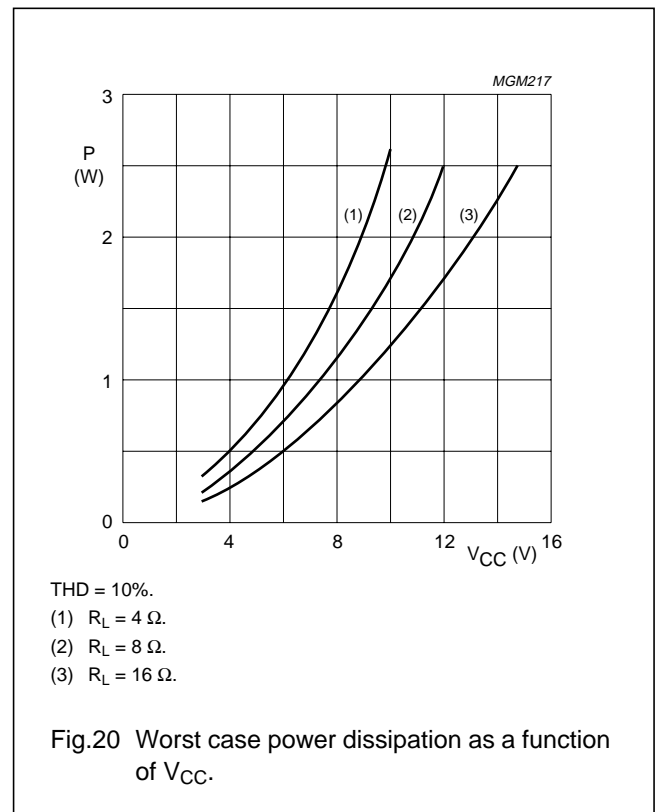
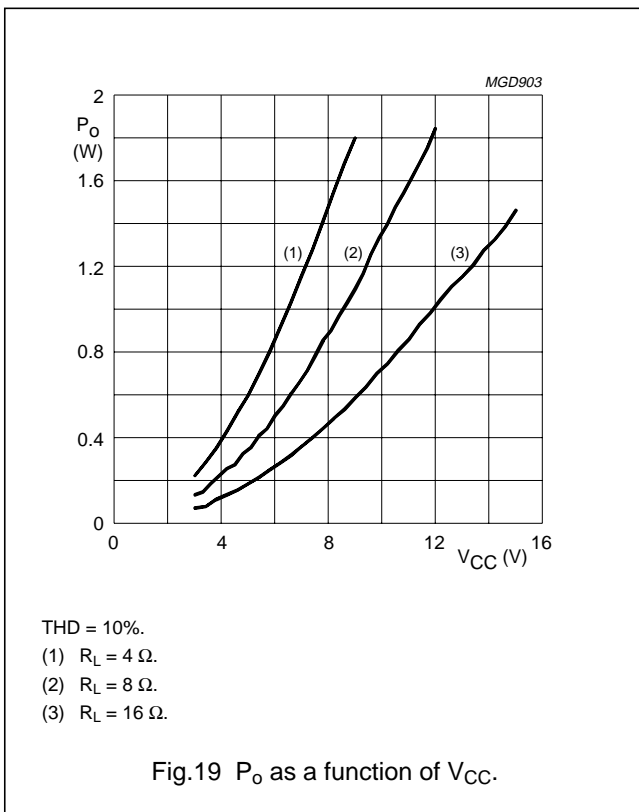
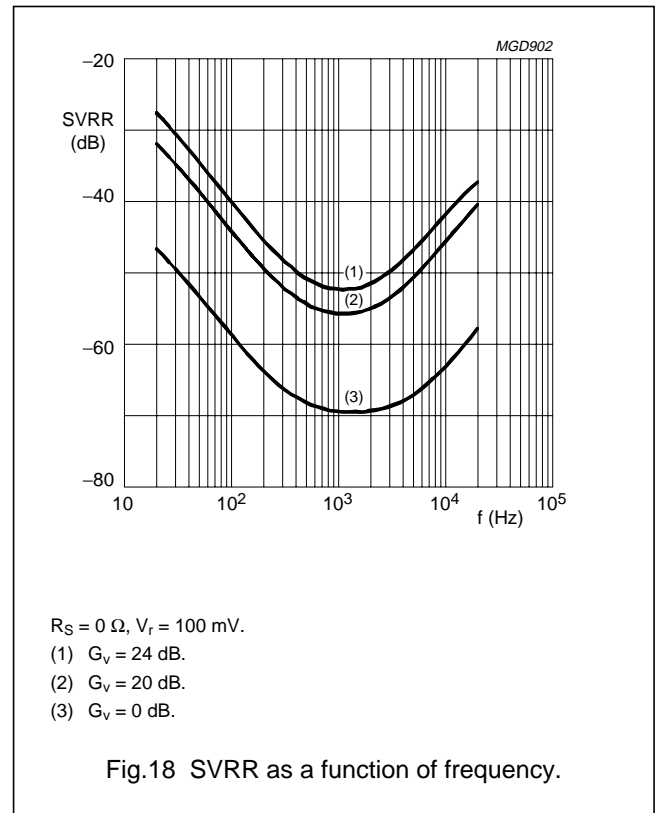
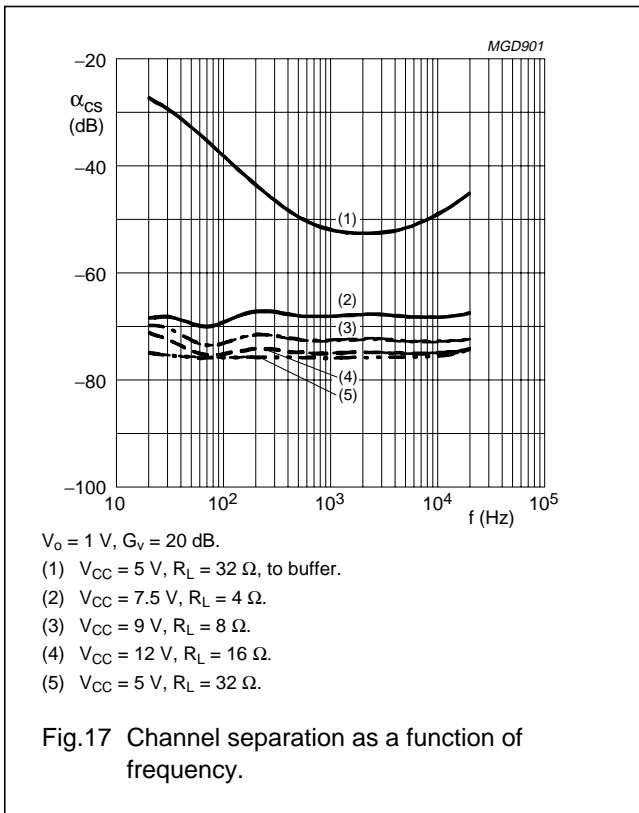
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SE APPLICATION



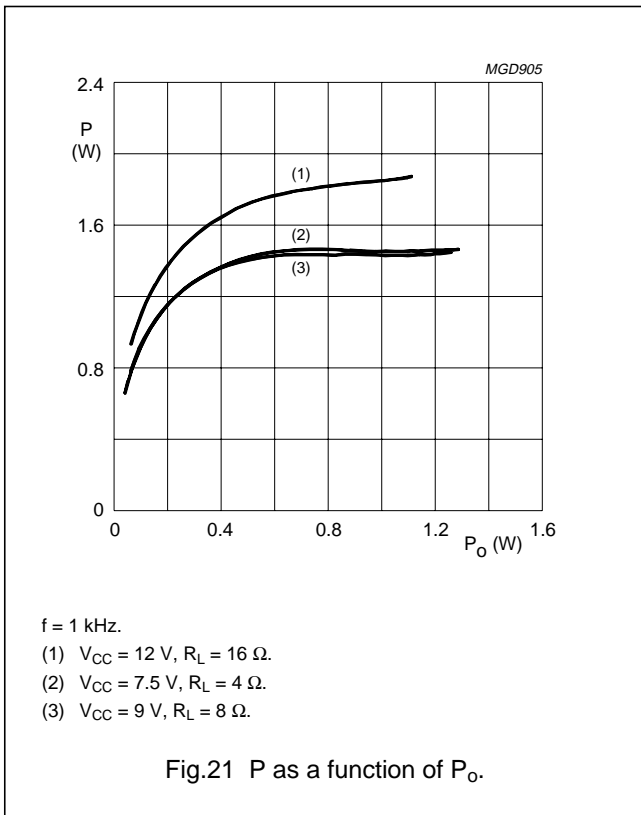
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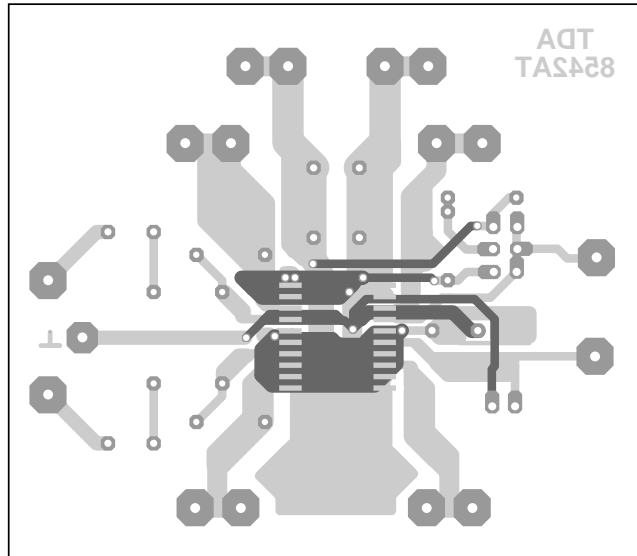
2 × 1.5 W BTL audio amplifier

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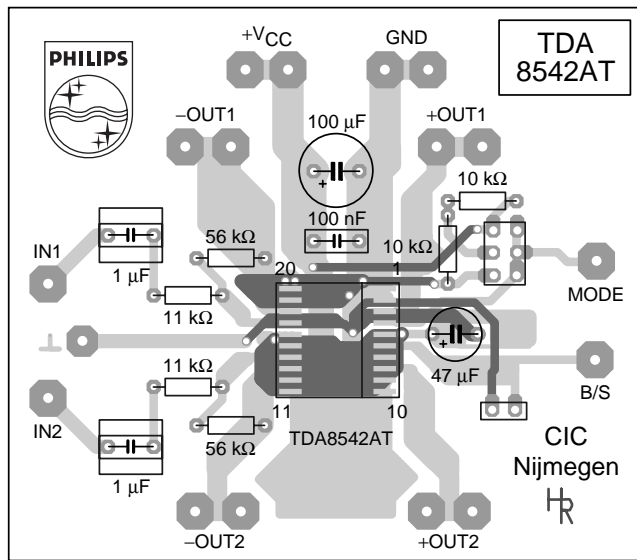


2 × 1.5 W BTL audio amplifier

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a. Top view of copper.



b. Top view with components.

Fig.22 Printed-circuit board layout (BTL).

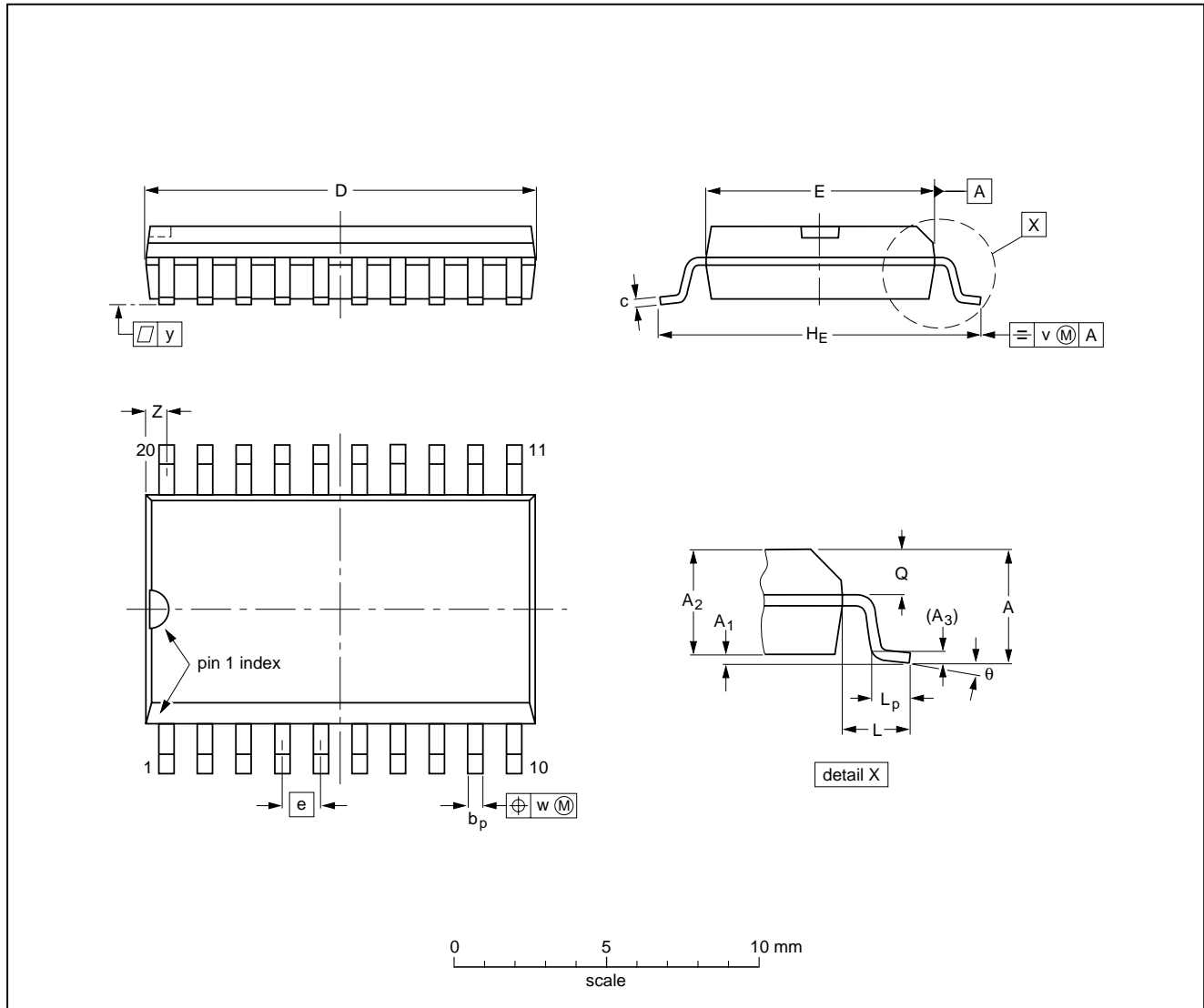
2 × 1.5 W BTL audio amplifier

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PACKAGE OUTLINE

SO20: plastic small outline package; 20 leads; body width 7.5 mm

SOT163-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	b <sub>p</sub>	c	D <sup>(1)</sup>	E <sup>(1)</sup>	e	H <sub>E</sub>	L	L <sub>p</sub>	Q	v	w	y	z <sup>(1)</sup>	θ
mm	2.65	0.30 0.10	2.45 2.25	0.25	0.49 0.36	0.32 0.23	13.0 12.6	7.6 7.4	1.27	10.65 10.00	1.4	1.1 0.4	1.1 1.0	0.25	0.25	0.1	0.9 0.4	8° 0°
inches	0.10	0.012 0.004	0.096 0.089	0.01	0.019 0.014	0.013 0.009	0.51 0.49	0.30 0.29	0.050	0.419 0.394	0.055	0.043 0.016	0.043 0.039	0.01	0.01	0.004	0.035 0.016	

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT163-1	075E04	MS-013AC				95-01-24 97-05-22



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**2 × 1.5 W BTL audio amplifier****TDA8542AT**

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**SOLDERING****Introduction**

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our *"IC Package Databook"* (order code 9398 652 90011).

**Reflow soldering**

Reflow soldering techniques are suitable for all SO packages.

Reflow soldering requires solder paste (a suspension of fine solder particles, flux and binding agent) to be applied to the printed-circuit board by screen printing, stencilling or pressure-syringe dispensing before package placement.

Several techniques exist for reflowing; for example, thermal conduction by heated belt. Dwell times vary between 50 and 300 seconds depending on heating method. Typical reflow temperatures range from 215 to 250 °C.

Preheating is necessary to dry the paste and evaporate the binding agent. Preheating duration: 45 minutes at 45 °C.

**Wave soldering**

Wave soldering techniques can be used for all SO packages if the following conditions are observed:

- A double-wave (a turbulent wave with high upward pressure followed by a smooth laminar wave) soldering technique should be used.
- The longitudinal axis of the package footprint must be parallel to the solder flow.
- The package footprint must incorporate solder thieves at the downstream end.

During placement and before soldering, the package must be fixed with a droplet of adhesive. The adhesive can be applied by screen printing, pin transfer or syringe dispensing. The package can be soldered after the adhesive is cured.

Maximum permissible solder temperature is 260 °C, and maximum duration of package immersion in solder is 10 seconds, if cooled to less than 150 °C within 6 seconds. Typical dwell time is 4 seconds at 250 °C.

A mildly-activated flux will eliminate the need for removal of corrosive residues in most applications.

**Repairing soldered joints**

Fix the component by first soldering two diagonally-opposite end leads. Use only a low voltage soldering iron (less than 24 V) applied to the flat part of the lead. Contact time must be limited to 10 seconds at up to 300 °C. When using a dedicated tool, all other leads can be soldered in one operation within 2 to 5 seconds between 270 and 320 °C.